

Attorney's Docket No. 016820P278

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Patent Application of:

Jay K. Gupta et al.

Application No.: Not yet assigned

Filed: February 11, 2004

For: METHOD OF MAXIMIZING
BANDWIDTH EFFICIENCY IN A
PROTOCOL PROCESSOR

Examiner: Not yet assigned

Art Unit: Not yet assigned

Commissioner for Patents
PO Box 1450
Alexandria, Virginia 22313-1450

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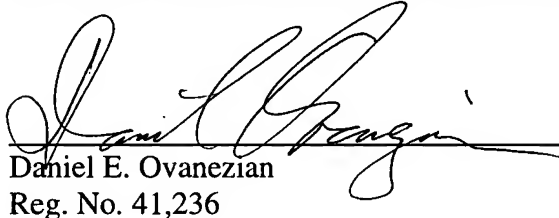
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If there are any additional charges, please charge Deposit Account No. 02-2666.

Respectfully submitted,

BLAKELY, SOKOLOFF, TAYLOR & ZAFMAN LLP

Dated: 2/11, 2004


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		First Named Inventor: Jay K. Gupta	
		Filing Date: February 11, 2004	
OTHER ART – NO PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	Translation ²
		MICHAEL TIMOTHY MOORE, Filling the WAN-communications-line card, EDN Magazine, April 18, 2002, pages 83-90.	
		LOUIS e. FRENZEL, Programmable Framer Chip Improves OC-48 Efficiency, Electronic Design, Downloaded April 16, 2001, 4 pages.	
		NILAM RUPARELIA, Delivering Ethernet over Sonet using Virtual Concatenation, CommsDesign.com, http://www.commsdesign.com/story/OEG20020225S078 , February 25, 2002, 8 pages.	

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